Responsivity of porous silicon for blue visible light with high sensitivity

Massar A. Kaood and Iftikhar M. Ali

Department of Physics, College of Science, University of Baghdad

E-mail: iftikhariq@gmail.com

Abstract

In this work, porous silicon (PS) are fabricated using electrochemical etching (ECE) process for p-type crystalline silicon (c-Si) wafers of (100) orientation. The structural, morphological and electrical properties of PS synthesized at etching current density of (10, 20, 30) mA/cm$^2$ at constant etching time 10 min are studied. From X-ray diffraction (XRD) measurement, the value of FWHM is in general decreases with increasing current density for p-type porous silicon (p-PS). Atomic force microscope (AFM) showed that for p-PS the average pore diameter decreases at 20 mA. Porous silicon which formed on silicon will be a junction so I-V characteristics have been studied in the dark to calculate ideality factor ($n$), and saturation current ($I_s$) for these junctions. These junctions are used in photosensors applications, where the photosensors have been examined at blue light region. Sensitivity, rise and fall times have been calculated for this wavelength, the maximum value for sensitivity is (3797.6%) at etching current density 10 mA/cm$^2$ under blue light illumination at zero bias voltage.

Key words

Porous silicon, photo sensor, responsivity, sensitivity.

Introduction

Porous silicon is an interesting and versatile optical material that is fabricated by electrochemical etching of silicon. Porous silicon technologies have many applications in semiconductor technology [1]. In the early 1990 s, the discovery of room
temperature photoluminescence from porous silicon by Canham who started a renewed interest in this material [2]. Since then, porous silicon has been developed as a means of increasing the functionality of silicon technology. The physical properties of porous silicon are fundamentally determined by the shape, diameter of pores, porosity, and the thickness of the formed porous layer. Depending on the etching parameters, for example current density, HF concentration, or substrate doping type and level, the physical properties of PS can be varied [3].

Photoconductivity (PC) is defined as electrical conductivity resulting from photo-induced electron excitations in which light is absorbed. In semiconductors, photoconductivity arises due to interaction of photons with bound electrons of lattice atoms that leads to photo-generation of electron-hole pairs after absorption of photons which increases carrier density and conductivity of material [4].

Since last decade the photoconductive properties of the inorganic nanoparticles have become subject of intensive study. Not only because of fundamental interests in the nature of the electronic excitations but also due to their applications in wide range of optical and electronic devices [5].

PC is an important tool to study the properties of semiconducting materials such as the nature of photo-excitation and recombination processes. The conductivity of material depends upon the carrier density, carrier lifetime and complex process of carrier generation, trapping, and recombination [6]. It is also a function of temperature, applied field, intensity of light and energy of radiation [7]. Extensive study of photoconductivity has been made in nanoparticles, thin film, nanorods, nanowires and mixed lattice [8, 9] for different parameters. A good photoconductive device requires not only efficient charge separation but also efficient transport of charge carriers to electrode [10].

In this work, p-PS have been used as photo sensor which is sensitive to light in the visible range which proves the success formation for porous process.

Experimental procedure

PS layer was prepared by electrochemical anodic etching of Si wafer: B (p-type)(100) oriented silicon substrate with a resistivity 0.01–0.02 Ω.cm. Before electrochemical etching process, the silicon wafers are cleaned with a solution HF: ethanol with a volume ratio 1:10 for a period of about 1 minutes. Thin film layer of Ag has been deposited on the wafers by electrodeposition method where wafers are immersed into a solution of 0.1 M silver nitrate solution that dissolved in 100 ml of distilled water. Silicon connected to the circuit with power supply in which the face of silicon opposite to platinum pole that connected to the positive end of the power supply with 3V that supplies current equals to 20 mA for 5 minutes. Silicon connected to the negative end of power supply, this Ag atoms work as a catalyst making the etching process easier. Then silicon wafers are cut into small pieces (1.3 × 1.3 cm²). The electrochemical etching process has been carried out at room temperature using Teflon materials which are adopted to form the main-body of hydrofluoric acid (HF) based electrolyte container. A mixture containing HF (40%), ethanol of (99%) and hydrogen peroxide (HF: ethanol: H₂O₂ = 1:4:1 volume ratio) were used as the etching solvent for all PS samples. H₂O₂ was added to the (HF) solution in order to improve the wettability of the acid and to allow
diffusion for the F ions into pores and to improve the PS layer uniformity by removing the hydrogen bubbles and helps to moisten silicon surface and improve reproducibility [11]. The anodization is done at room temperature under constant time of 10 minutes for etching current densities (10, 20, 40) mA/cm$^2$, as shown in Fig.1.

**Fig.1: The electrochemical etching set-up.**

**Result and discussion**

1. X-Ray diffraction results

Fig. 2 shows the x-ray diffraction of porous silicon p-type (100) direction at different current densities (10, 20, 40) mA/cm$^2$ for constant etching time of 10 min. XRD patterns show the formation of porous silicon where the strong peak of Si is observed at 2$\theta$ = 68.002° to p-type for (100) direction. The broadening in this peak proves the nanosize effect where the large value of FWHM (2.5230) at 10 mA/cm$^2$ may be due to the small crystallites that illustrate there is a control on the nanosize of porous layer. The formation of porous layer results in lattice expansion or the strain that caused by the effect of nanostructure that make small shift in the peak positions [12]. It is observed from Table 1 that the value of FWHM is in general decreases with increasing current density. The crystallite size is calculated by using Debye Scherrer formula in Eq. (1):

$$D=K \frac{\lambda}{\beta \cos \theta}$$

where $\lambda$ is the X-ray wavelength (1.54 Å), $\beta$ is full width at half maximum (FWHM), $\theta$ is Bragg's angle, K is the Scherrer's constant which was ranged from (0.9 to 1) depends on geometry of structure. It is clear from these calculations that there is an increase in crystallite size from 3.8 to 39.6 nm for highest peak for p-type silicon, other parameters in the table are calculating corresponding to the highest peak in Fig. 2. Also this figure shows that there is SiO$_2$ phase which proves the formation of porous silicon.

**Fig.2: X-ray diffraction of p-PS prepared at current densities (10, 20, 40) Ma/cm$^2$ and 10 min etching time.**
Table 1: Structural parameters: $2\theta$, $d_{hkl}$ (hkl), FWHM and crystallite size ($D$) of p-PS at current densities (10, 20 and 40) mA/cm$^2$ at 10 min etching time.

<table>
<thead>
<tr>
<th>I (Ma)</th>
<th>$2\theta$ (Deg.)</th>
<th>FWHM (Deg.)</th>
<th>$d_{hkl}$ Exp.(Å)</th>
<th>$D$ (nm)</th>
<th>hkl</th>
<th>$d_{hkl}$ Std.(Å)</th>
<th>Phase</th>
<th>Card No.</th>
</tr>
</thead>
<tbody>
<tr>
<td>10</td>
<td>68.0020</td>
<td>2.5230</td>
<td>1.3781</td>
<td>3.8</td>
<td>(400)</td>
<td>1.3577</td>
<td>Cub. Si</td>
<td>96-410-4918</td>
</tr>
<tr>
<td>20</td>
<td>67.8934</td>
<td>1.5420</td>
<td>1.3794</td>
<td>6.2</td>
<td>(400)</td>
<td>1.3577</td>
<td>Cub. Si</td>
<td>96-410-4918</td>
</tr>
<tr>
<td>40</td>
<td>68.2000</td>
<td>0.5000</td>
<td>1.3740</td>
<td>19.2</td>
<td>(400)</td>
<td>1.3577</td>
<td>Cub. Si</td>
<td>96-410-4918</td>
</tr>
</tbody>
</table>

2. Atomic Force Microscopy (AFM) analysis results

The morphological properties of p-PS samples prepared with different current densities (10, 20 and 40) mA/cm$^2$ at 10 min etching time are shown in Fig. 3. Pores are observed on the surface of the layer to be distributed uniformly. The pores have sphere-like shape and the porous layers have sponge-like appearance. As shown in Table 2 the average pore diameter decreases at 20 mA because the effect of chemical dissolution and current density increase and this make reducing in the wall separated between the pores which grow preferentially towards the source of positive carriers (holes), which are involved in the electrochemical dissolution reaction [13].

Fig. 3: 2D, 3D AFM images and size distribution diagrams of meso - porous silicon (p-type) prepared at different current densities; (a) 10, (b) 20, (c) 40 mA/cm$^2$. 

101
Table 2: The calculated morphology characteristics of p-PS samples prepared at different etching current densities.

<table>
<thead>
<tr>
<th>Current density (mA/cm$^2$)</th>
<th>Roughness Average (nm)</th>
<th>r.m.s roughness (nm)</th>
<th>Average Pore Diameter (nm)</th>
<th>Average Pore Height (nm)</th>
</tr>
</thead>
<tbody>
<tr>
<td>10</td>
<td>0.322</td>
<td>0.379</td>
<td>38.31</td>
<td>1.50</td>
</tr>
<tr>
<td>20</td>
<td>0.344</td>
<td>0.399</td>
<td>34.19</td>
<td>1.44</td>
</tr>
<tr>
<td>40</td>
<td>0.252</td>
<td>0.298</td>
<td>54.43</td>
<td>1.21</td>
</tr>
</tbody>
</table>

3. Fourier- transform infrared (FTIR)

Fig. 4 shows, the FTIR spectra measured for porous silicon at current density (20 mA/cm$^2$) and etching time (10 min), the peaks at around (486.762) cm$^{-1}$ Si–O stretching in Si-O-Si mode, which are dependent on the oxidation degree of PSi. The transmittance peak at (1046.94) cm$^{-1}$ Si-O stretching in O-Si-O and C-Si-O [14]. While the peak at (1615.03) CH$_2$Wagging C=C stretch mode and a strong broad band is observed at (3407.98) cm$^{-1}$ O-H mode.

![FTIR spectrum](image)

**Fig.4:** FTIR transmittance spectrum for PSi (n-type) at (20 mA/cm$^2$) and etching time (10min).

Table 3: Wavenumber positions and bonds of the transmittance peaks observed in several PS samples by Fourier transform infrared absorption FTIR measurements.

<table>
<thead>
<tr>
<th>p-PS</th>
<th></th>
</tr>
</thead>
<tbody>
<tr>
<td>Peak position</td>
<td>Bound</td>
</tr>
<tr>
<td>(cm$^{-1}$)</td>
<td></td>
</tr>
<tr>
<td>486.762</td>
<td>Si-O stretching in</td>
</tr>
<tr>
<td></td>
<td>Si-O-Si</td>
</tr>
<tr>
<td>1046.94</td>
<td>Si-O stretching in</td>
</tr>
<tr>
<td></td>
<td>O-Si-O and C-Si-O</td>
</tr>
<tr>
<td>1615.03</td>
<td>CH$_2$Wagging C=C</td>
</tr>
<tr>
<td></td>
<td>stretch</td>
</tr>
<tr>
<td>3407.98</td>
<td>O-H</td>
</tr>
</tbody>
</table>
4. I-V characteristics for p-PS in the dark

The dark I–V characteristics at different current densities of (10, 20, 40) mA/cm$^2$ at constant etching time of 10 min for the p-PS hetero junction. It is obvious that the curve has a non-linear behavior this indicates that the electrical contact is not ohmic as shown in Fig. 5.

![Fig. 5: I-V characteristics in the dark for p-PS.](image)

In general the forward dark current is generated due to the flow of majority carriers and the applied voltage inject majority carriers which leads to decrease the value of built-in potential, and decrease the width of the depletion layer. Then the majority and minority carrier concentration is higher than the intrinsic carrier concentration ($n_i^2 < np$) which leads to generate recombination current at the low voltage region (0-0.3) V because that the excitation electrons from V.B to C.B will recombine with the holes which found at the V.B, and this is observed by little increase in recombination current at low voltage region. The tunneling current has been represented at the high voltage region (>0.3V). After that there is a fast exponential increase in the current magnitude with increasing the voltage and this is called diffusion current. Also the reverse bias current which also contain two regions, in the first region (low voltages<0.3V) the current slightly increases with increasing the applied voltage, and the generation current dominates, while at the high voltage region (>0.3V), the diffusion current dominates [15]. The value of saturation current $I_s$, ideality factor $n$ and are calculated from Eq. (2) as shown in Fig. 6 and their values are tabulated in Table 4:

\[ n = \frac{q}{k_B T} \left( \frac{V}{\ln(I_f/I_s)} \right) \]

where $V$ and $I_f$ are the forward bias voltage and forward current respectively, $q$ is electronic charge, $k_B$ Boltzman constant and $T$ temperature which is 300 K.
Fig. 6: Variation of $\ln(J/J_s)$ vs. bias voltage to extract ideality factor.

Table 4: $I_s$ and $n$ for p-PS at different etching current densities in dark.

<table>
<thead>
<tr>
<th>Etching current density (mA/cm$^2$)</th>
<th>$I_s \times 10^6$ A</th>
<th>$n$</th>
</tr>
</thead>
<tbody>
<tr>
<td>10</td>
<td>0.19</td>
<td>5.94</td>
</tr>
<tr>
<td>20</td>
<td>0.25</td>
<td>2.67</td>
</tr>
<tr>
<td>40</td>
<td>0.85</td>
<td>5.12</td>
</tr>
</tbody>
</table>

The current-voltage measurements have been done under light with intensity 287.65 mW/cm$^2$ from Halogen lamp, as shown in Fig. 7.

Fig. 7: I-V characteristics under white light for p-PS at current densities (10, 20 and 40) mA/cm$^2$.

5. Responsivity $R_\lambda$ for p-PS

The light responsivity of the devices is measured in the wavelength range of (200–900) nm under zero bias voltage. Fig. 8 shows the measured maximum values of responsivity at the labeled wavelengths for anodization current densities of (10, 20 and 40) mA/cm$^2$. In general, at blue light region there is maximum responsivity obtained for our devices.
Fig. 8: The variation of spectral responsivity of p-PS as a function of the wavelength at different anodization current densities of (10, 20 and 40) mA/cm$^2$ at zero bias voltage.

6. The sensitivity – time (S-t) characteristics

The sensitivity-time (S-t) characteristics are taken at 460 nm which is in the blue region without bias voltage at different current densities and constant etching time 10 min. When the light was turned on, conductivity increased and after the light was turned off, the current returned to its original value. This process was repeated many times as seen in Fig. 9, one can see the stability of the response with time. From our data, it appears to be possible to control the response of the current in a semiconducting photodetector because the electrons in the nanoparticles receive their excitation energy from the power of the light source, it is possible to “switch” these nanostructures reversibly between higher and lower states of conductivity.

As we see from the figures, the time required around 0.7 s to travel from its lowest point to its highest and nearly
from 1~3 second to retreat once the light was turned off. Table 5 shows the sensitivity, rise and fall times in which the sensitivity reaches to maximum value (3797.6 %) at etching current density 10 mA/cm² for blue light. Samples are switched on and off periodically at a regular period of time.

![Fig.9: Sensitivity, responsivity, rise and fall times of the p-PS photo sensors under the wavelength of 460 nm without applied voltage at different etching current densities.](image)

**Table 5: The sensitivity, rise and fall times values for p-PS with different etching current densities.**

<table>
<thead>
<tr>
<th>Current density (mA/cm²)</th>
<th>Sensitivity % at: 460 nm</th>
<th>Rλ (A/W) at blue region</th>
<th>τr (s)</th>
<th>τf (s)</th>
</tr>
</thead>
<tbody>
<tr>
<td>10</td>
<td>3797.6</td>
<td>0.987</td>
<td>0.7905</td>
<td>0.7782</td>
</tr>
<tr>
<td>20</td>
<td>257.4</td>
<td>0.103</td>
<td>0.7373</td>
<td>3.413</td>
</tr>
<tr>
<td>40</td>
<td>2707.5</td>
<td>1.077</td>
<td>0.7667</td>
<td>0.7687</td>
</tr>
</tbody>
</table>

**Conclusions**

It has shown that porous silicon is prepared successfully by electrochemical etching process with spherical pore shape which distributed uniformly within porous layer. The phase of this layer is mixed from Si and SiO₂ which is confirmed by XRD pattern and FTIR analysis. These devices have been used as photo sensor with high sensitivity reaches more than 3000 % for blue light with fast response time less than 1 second without any bias voltage.

**References**